CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Commissioner for Patents and Trademarks, Washington, D.C. 20231, on April 5, 2002.

Grace Alicea

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the name of:

OKUDA, et al.

Serial No.: 10/018,708

I.A. Filing Date: June 15, 2000

USPTO Filing Date: Dec. 13, 2001

Date: April 5, 2002

Group Art Unit: to be assigned

Examiner: to be assigned

Title: TABLE OF WAFER POLISHING APPARATUS, METHOD FOR POLISHING

SEMICONDUCTOR WAFER, AND METHOD FOR MANUFACTURING

SEMICONDUCTOR WAFER

Attention BOX MISSING PARTS **Assistant Commissioner of Patents** Washington, D.C. 20231

TRANSMITTAL LETTER

Sir:

In response to the Notification of Missing Requirements:

- 1) Notification of Missing Requirements Under 35USC 371 in the U.S. Designated/Elected Office DO/EO/US (copy);
- Response to Notice of Missing Requirements; 2)
- Declaration/Power of Attorney (combined) executed by the inventors;: 3)
- Check No. 4289 in the sum of \$130.00; 4)
- Recordation Form Cover Sheet and executed Assignment document; ; and 5)
- 6) Postcard.

If any unresolved issues remain, please contact Applicant's attorney at the telephone number below.

Respectfully submitted,

04/24/2002 MALILL

00000014 10018708

01 FC:154

130.00 OP

Attorney for Applicants

Reg. No. 30,801 (650) 493-4540

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Commissioner for Patents and Trademarks, Washington, D.C. 20231, on April 5, 2002.

Grace Alicea

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Application of:

Date: April 5, 2002

OKUDA, et al.

Serial No.: 10/018,708

Group Art Unit: to be assigned

I.A. Filing Date: June 15, 2000 USPTO Filing Date: Dec. 13, 2001 Examiner: to be assigned

For:

TABLE OF WAFER POLISHING APPARATUS, METHOD FOR POLISHING

SEMICONDUCTOR WAFER, AND METHOD FOR MANUFACTURING

SEMICONDUCTOR WAFER

Assistant Commissioner of Patents Washington, D.C. 20231

RESPONSE TO NOTIFICATION OF MISSING REQUIREMENTS UNDER 35 USC 371 IN THE U.S. DESIGNATED/ELECTED OFFICE DO/EO/US)

Sir:

In response to the Notification of Missing Requirements issued March 18, 2002, submitted herewith is a Declaration executed by the inventors.

The Commissioner is hereby authorized to charge any fees or credit any overpayment associated with this communication to Deposit Account 02-2120 (Sawyer Law Group LLP). If any unresolved issues remain, please contact Applicant's attorney at the telephone number indicated below.

Respectfully submitted,

05/24/2002 BCAMPBEL 00000006 022120

01 FC:966

360.00 CH

02 FC:968

280.00 CH

Attorney for Applicants

Reg. No. 30,801 (650) 493-4540